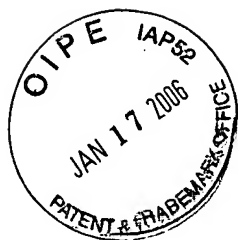


IFW

PATENT



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Sang-Hyun WOO et al.

Group Art Unit: 2681

Serial No.: 10/767,089

Docket: 784-60 (SI-19537-US)

Filed: January 29, 2004

Dated: January 12, 2006

For: **SINGLE CHIP DIRECT CONVERSION TRANSCEIVER FOR REDUCING DC OFFSET AND METHOD OF MANUFACTURING THE SAME**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Sir:

Pursuant to Applicant's continuing duty of disclosure, it is respectfully requested that the references listed in the attached form PTO-1449 be considered by the Examiner and made of record in the above-identified application. A copy of each reference is attached hereto.

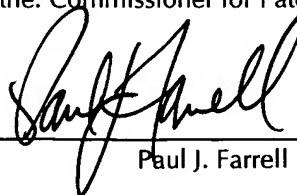
The citation of the listed items is not a representation that they constitute a complete or exhaustive listing of the relevant art or that the references are prior art. The items listed are submitted in good faith, but are not intended to substitute for the Examiner's search. It is hoped, however, that in addition to apprising the Examiner of these particular items, they will assist in identifying fields of search and in making as full and complete a search as possible.

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**CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope addressed to the: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on January 12, 2006.

Dated: January 12, 2006

  
Paul J. Farrell

The listed items were cited by the European Patent Office in a counterpart application, namely Appln. No. 04250492.8. A copy of the European Search Report dated November 30, 2005 is attached hereto.

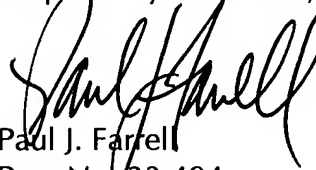
The filing of this Supplemental Information Disclosure Statement is not an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

The claims of the application as now presented are believed to patentably distinguish over the prior art and to be in condition for allowance. Early and favorable consideration of the case is respectfully requested.

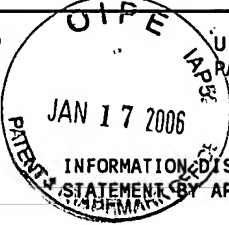
**CERTIFICATION UNDER 37 C.F.R. §1.97(e)(2)**

Applicant submits that each item of information contained in the Supplemental Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application no more than three months prior to the filing of the Statement.

Respectfully submitted,

  
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Form PTO-1449 	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 784-60	SERIAL NO. 10/767,089
	APPLICANT Sang-Hyun WOO et al.			
	FILING DATE January 29, 2004		GROUP ART UNIT 2681	

(Use several sheets if necessary)

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6,360,087	3/19/2002	Rozenblit et al.			

## FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

## OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

		1.	Sekiguchi et al., "Ultra Small Sized Low Noise Block Downconverter Module", June 1, 1992.
		2.	Jeff Durec, "An Integrated Silicon Bipolar Receiver Subsystem for 900MHz Band Applications", September 28, 1997.

EXAMINER	DATE CONSIDERED
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\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.